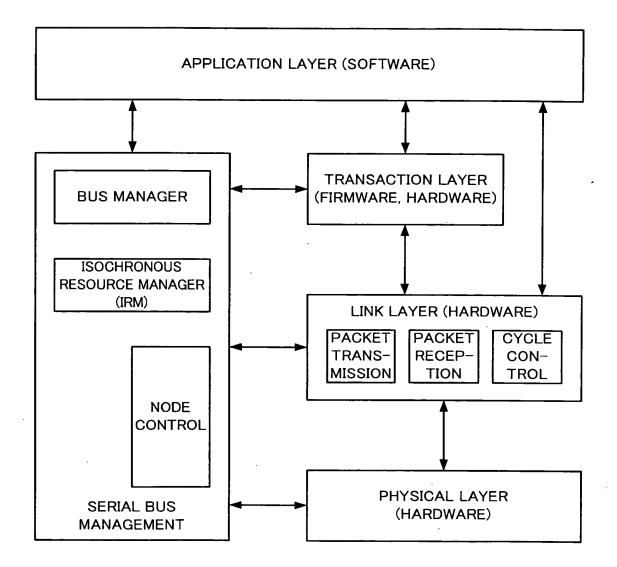
FIG.1



2/25

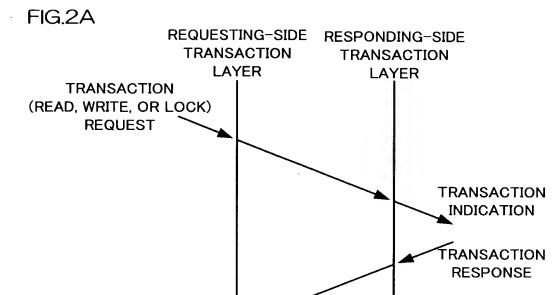


FIG.2B

TRANSACTION A CONFIRMATION

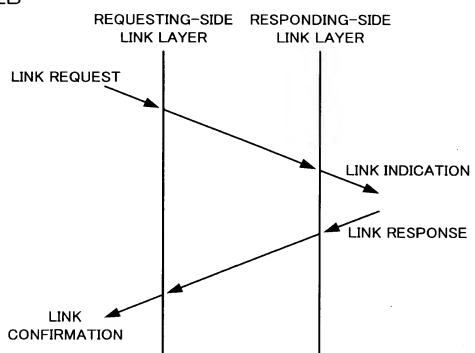
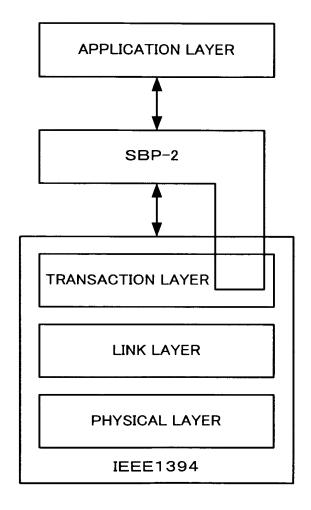
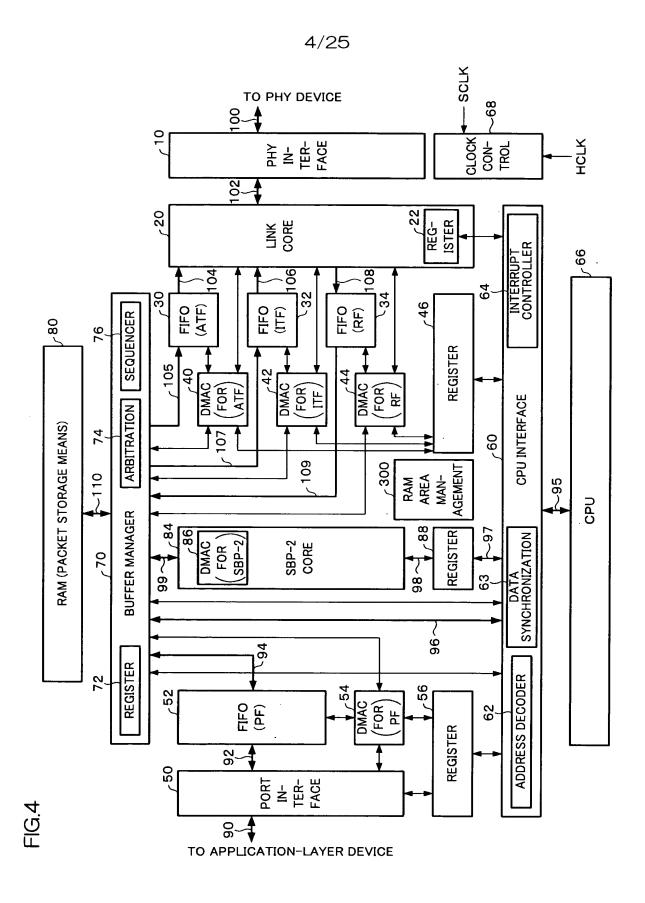


FIG.3

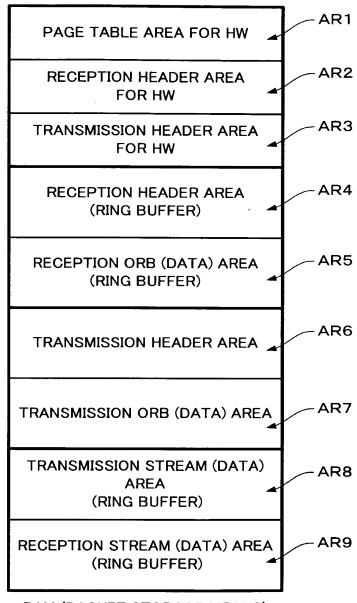
And the second section





### FIG.5

ROS 948 8 10 10 CONTRADOR SON



**RAM (PACKET STORAGE MEANS)** 

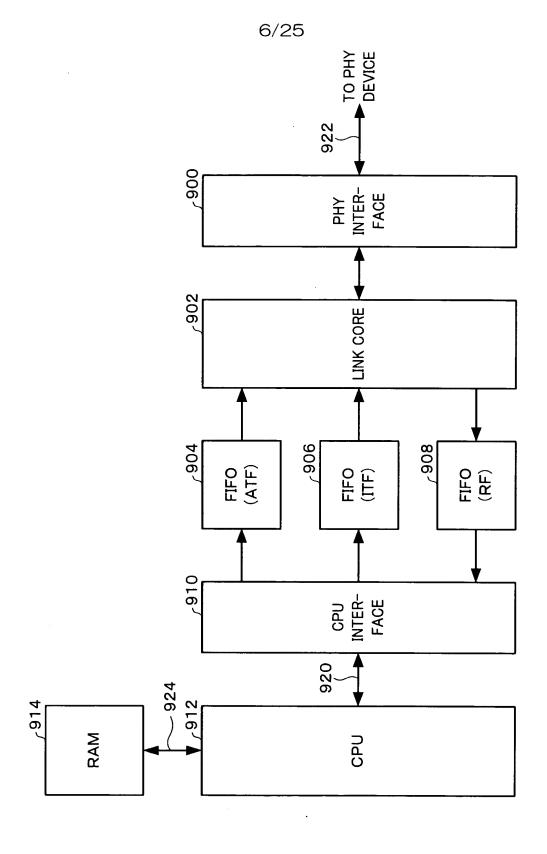
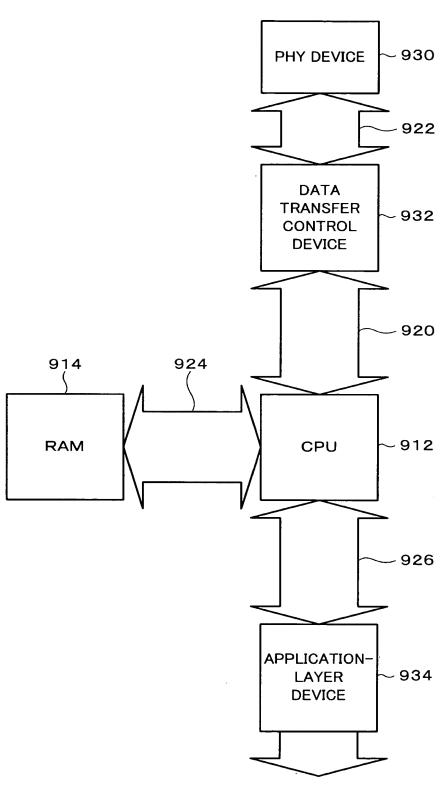
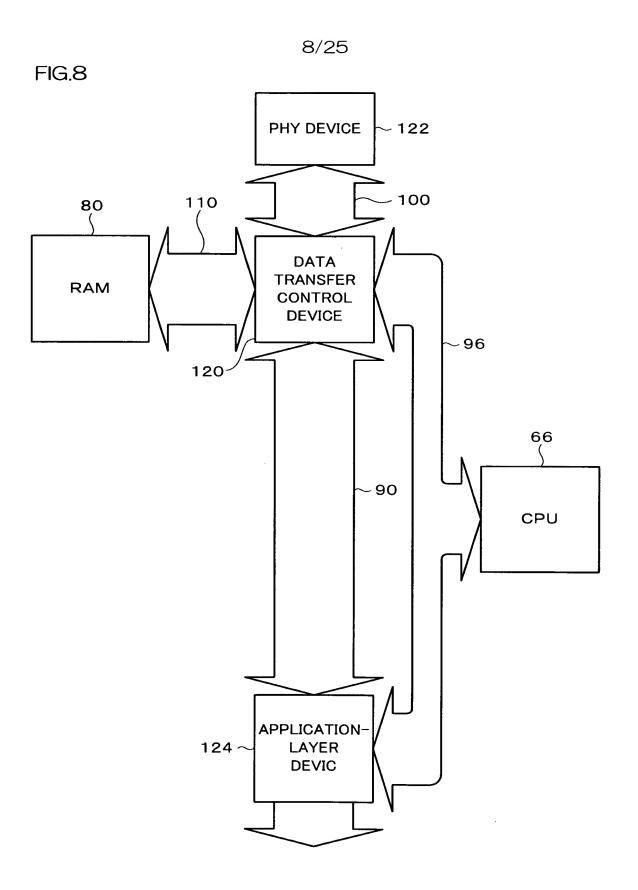


FIG.6

7/25









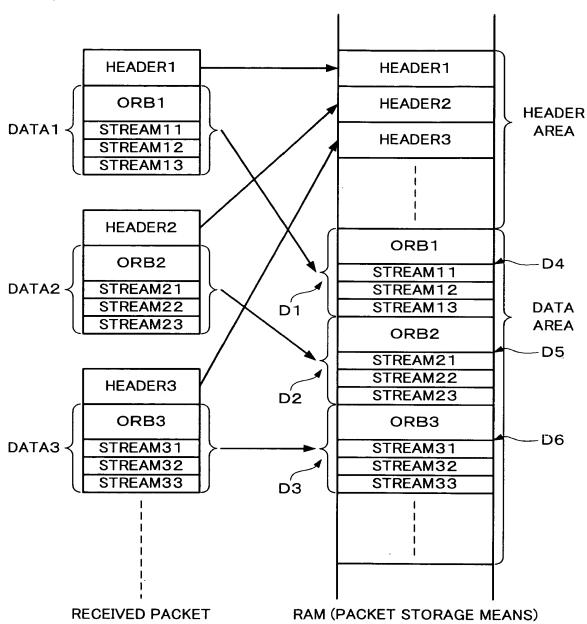
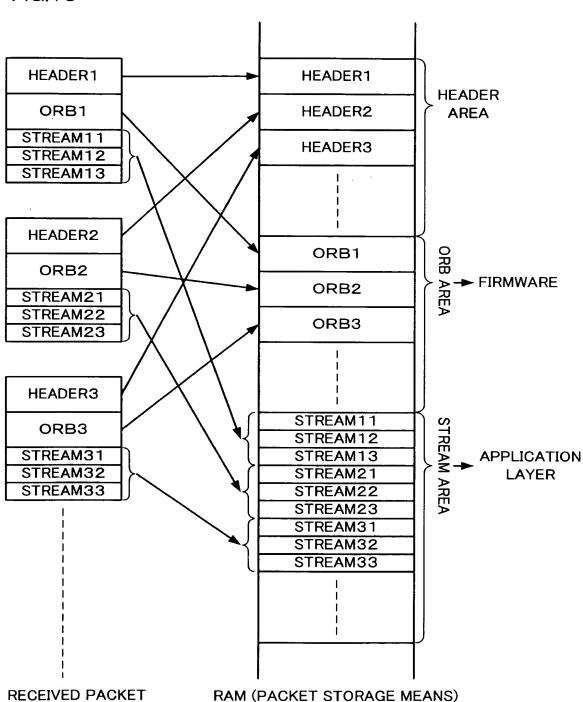


FIG.10



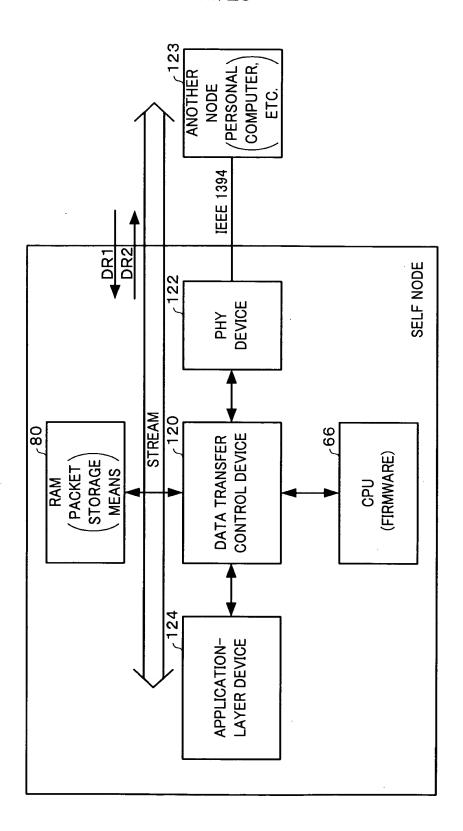


FIG.11

12/25

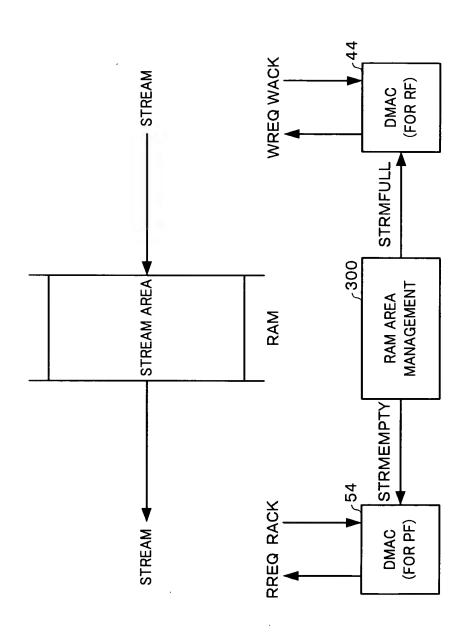


FIG.12

13/25

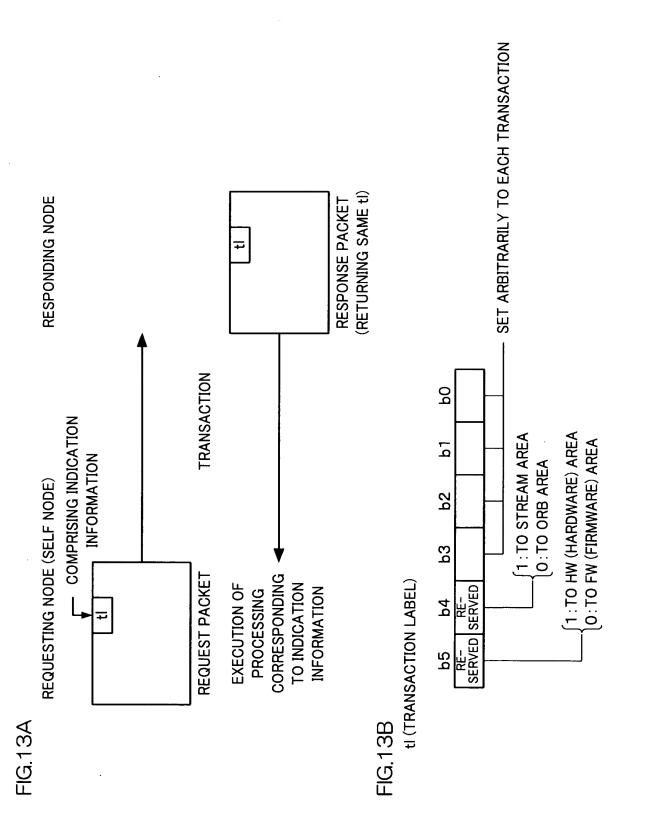
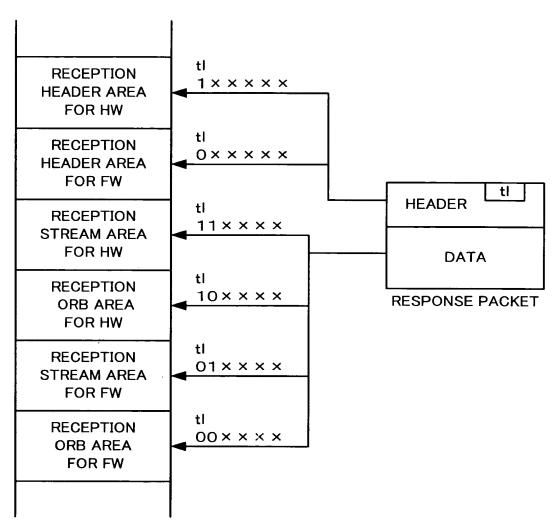


FIG.14



**RAM (PACKET STORAGE MEANS)** 

FIG.15

tara in a series and

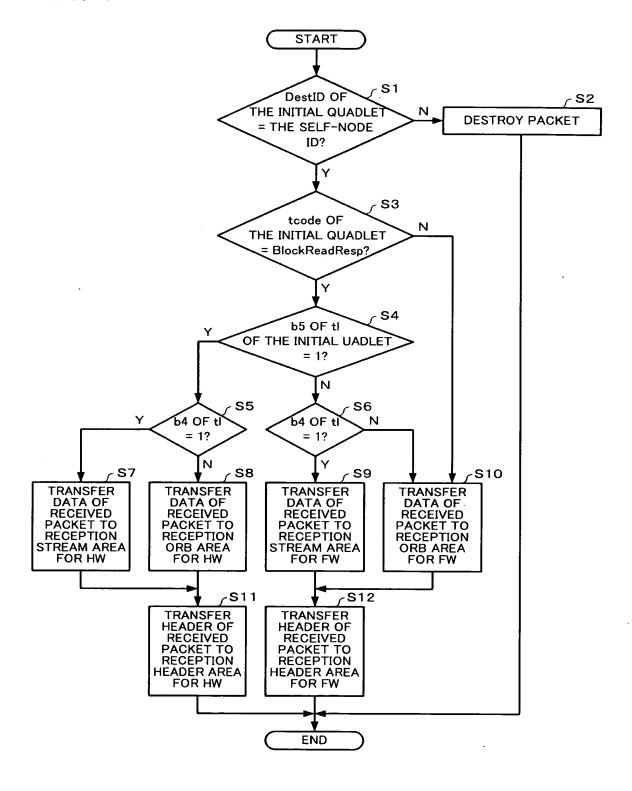
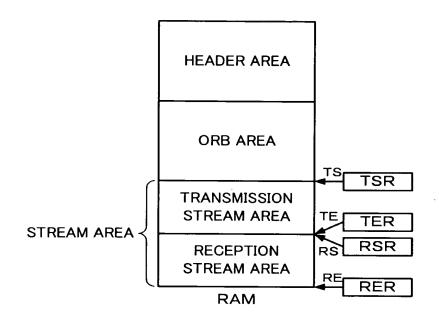
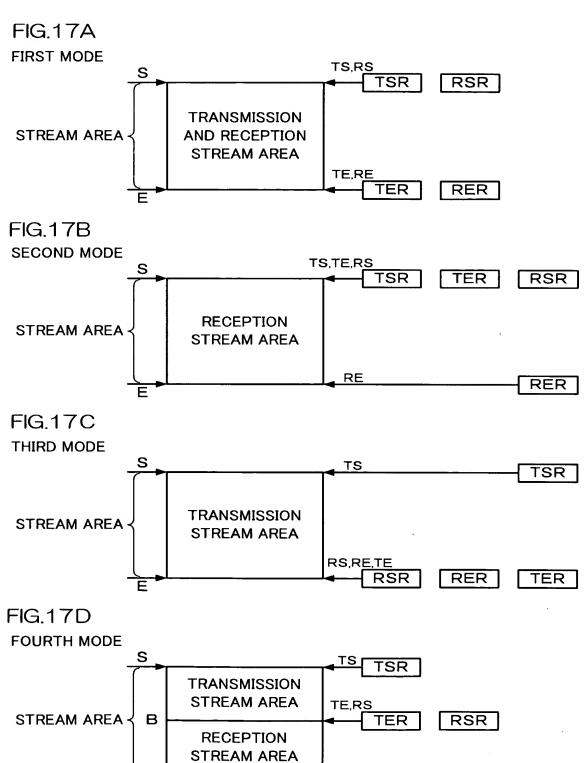


FIG.16

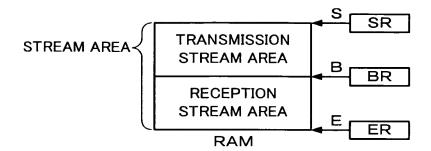


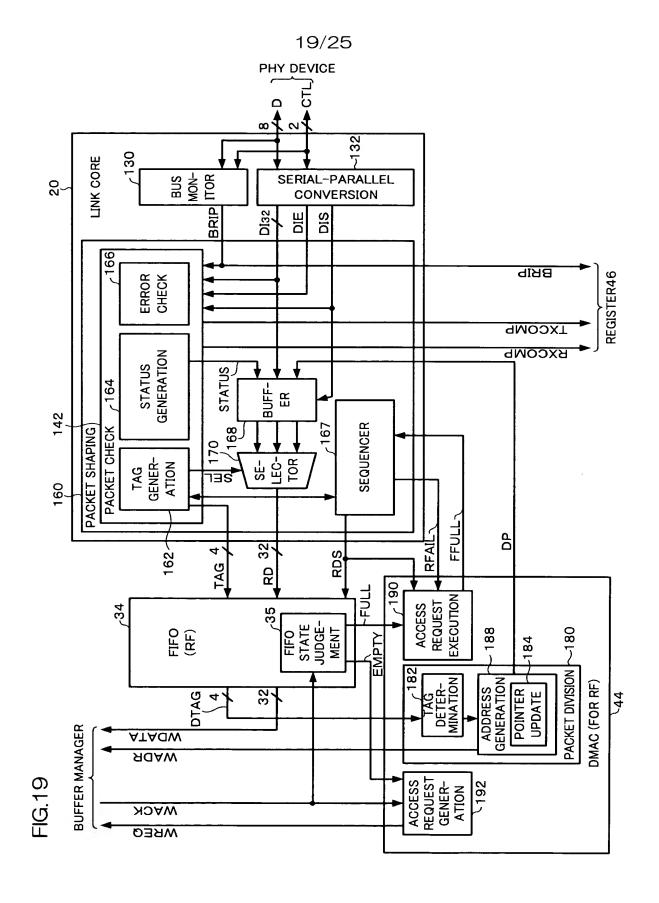


RE

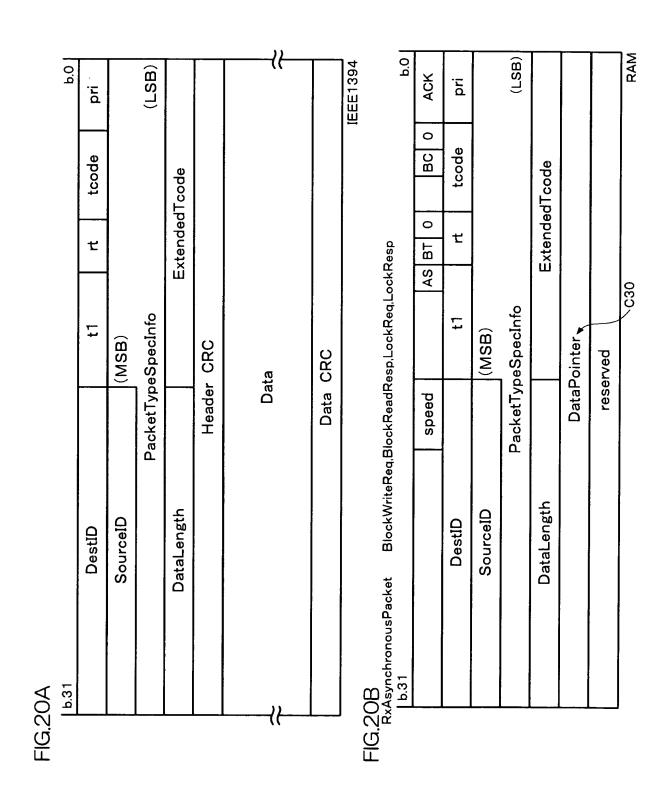
RER

FIG.18





20/25



# FIG.21

TAG(DTAG)	MEANING
0001	FW-SOP
0010	FW-HDR
0011	FW-FTR
0100	FW-ORB
0101	FW-STRM
1001	HW-SOP
1010	HW-HDR
1011	HW-FTR
1100	HW-ORB
1101	HW-STRM

FW ····· FIRMWARE HW ···· HARDWARE

SOP ····· START OF PACKET

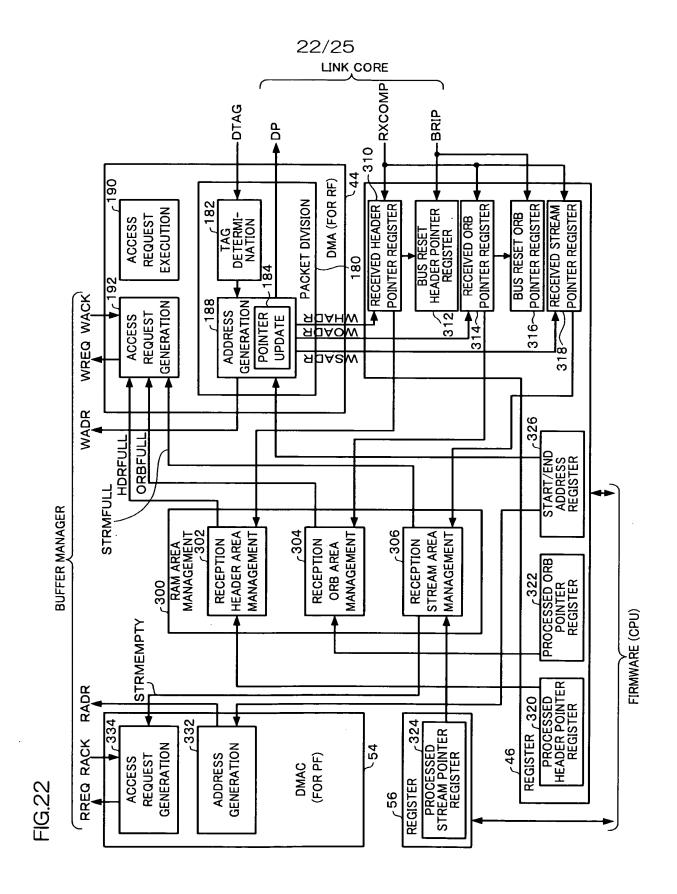
(INITIAL QUADLET OF RECEIVED PACKET)

HDR ..... HEADER OTHER THAN SOP

FTR ..... ACK TRANSMISSION

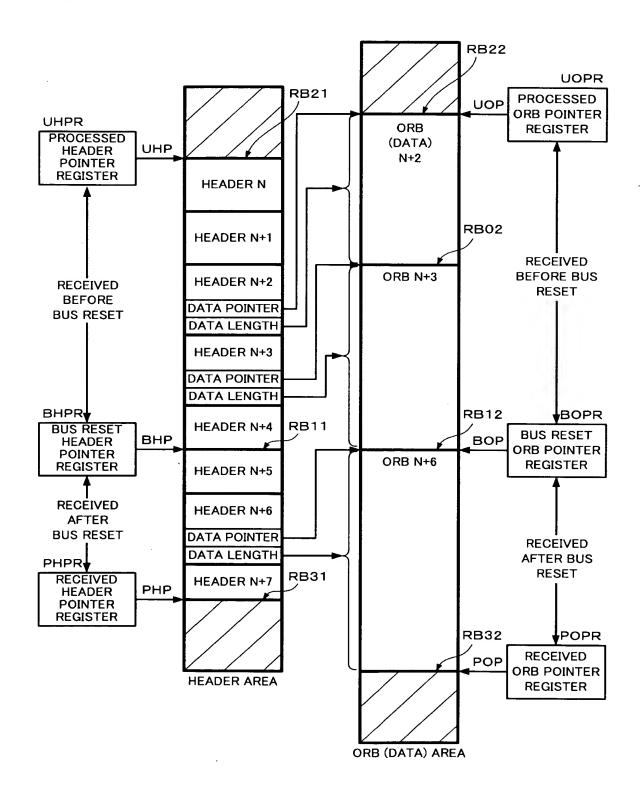
ORB ····· ORB (DATA)

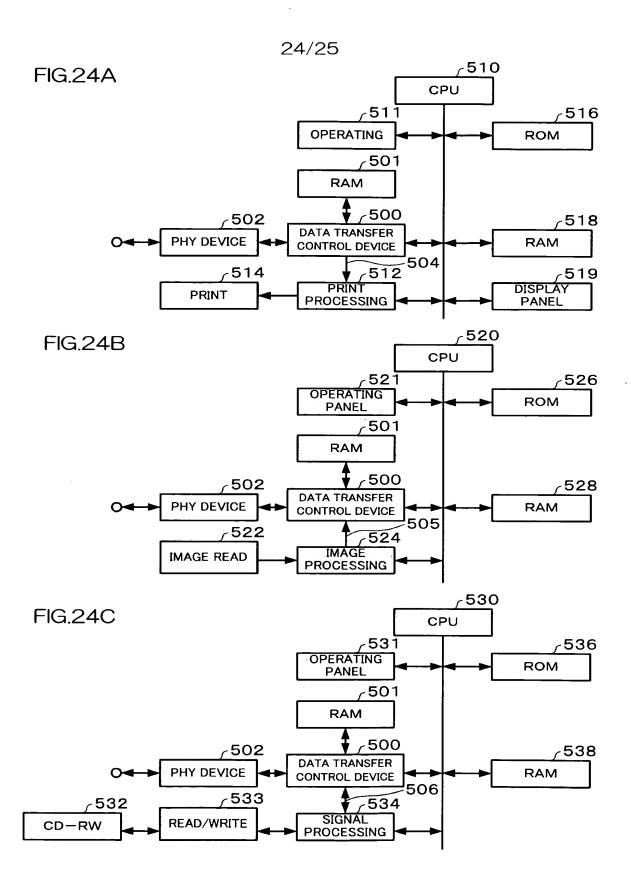
STRM · · · · · STREAM (DATA)



23/25

**FIG.23** 





25/25

FIG.25A

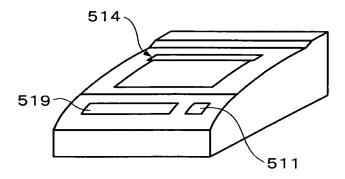


FIG.25B

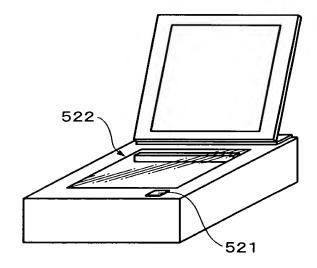


FIG.25C

